



PATENT  
452080-2010.2

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

PATENT & TRADEMARK OFFICE  
Applicant(s)

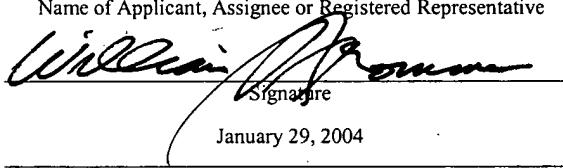
Serial No. : 09/852,999 Notice of Allowance  
Dated: January 8, 2004  
For : SEMICONDUCTING DEVICES AND METHOD OF  
MAKING THEREOF  
Confirmation No.: 5824  
Filed : May 10, 2001  
Examiner : Shrinivas H. Rao  
Art Unit : 2814

745 Fifth Avenue  
New York, NY 10151

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: **Mail Stop Issue Fee Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450**, on January 29, 2004.

William S. Frommer, Reg. No. 25,506

Name of Applicant, Assignee or Registered Representative

  
Signature  
January 29, 2004

Date of Signature

**FILING OF FORMAL DRAWINGS**

Mail Stop Issue Fee  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Attention: **Official Draftsperson**

Sir:

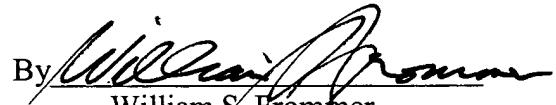
In accordance with the requirement cited in the Notice of Allowability (PTOL-37, Paper No. 7), Applicants file herewith (3) sheets of formal drawings to be made of record in the

present application in place of the original drawings and which avoid the objections stated in  
PTO Form 948 attached to Paper No. 11.

Respectfully submitted,

FROMMER LAWRENCE & HAUG LLP  
Attorneys for Applicants

By

  
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Encs. - Formal Drawings (3) Sheets